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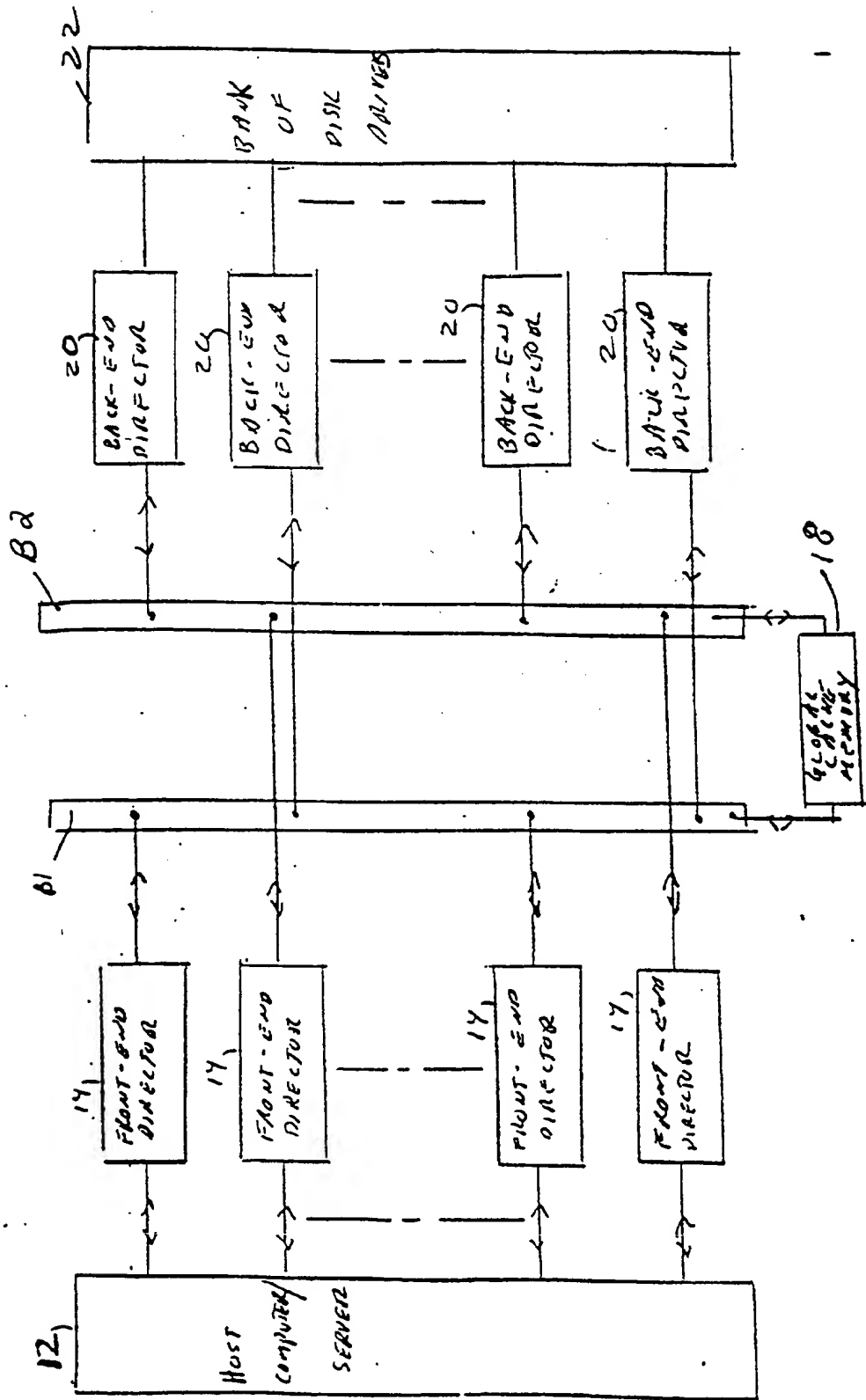
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FIG. 1
DATA NET



00500156 100500

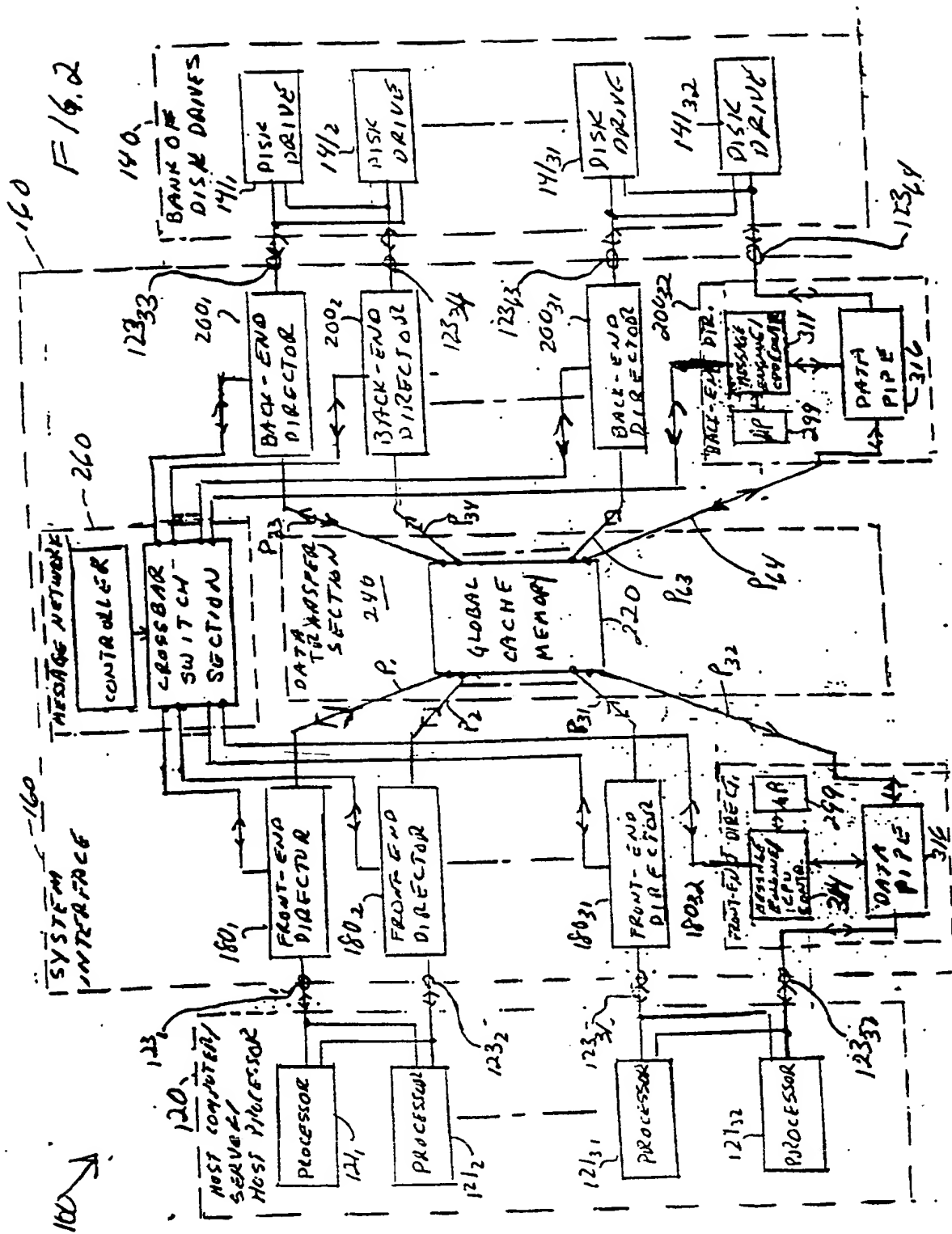


FIG. 3

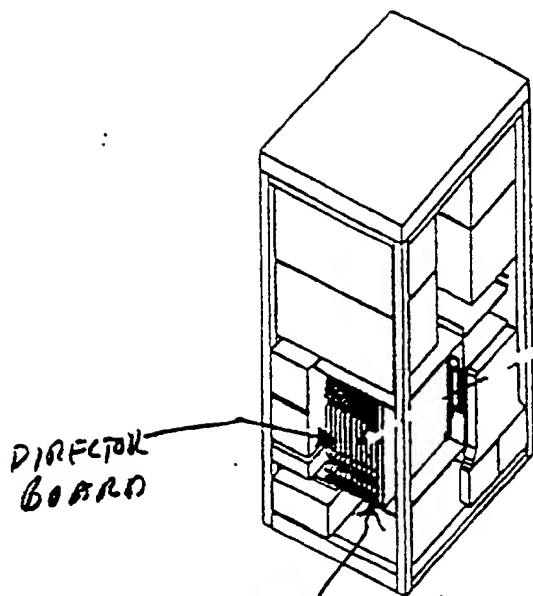
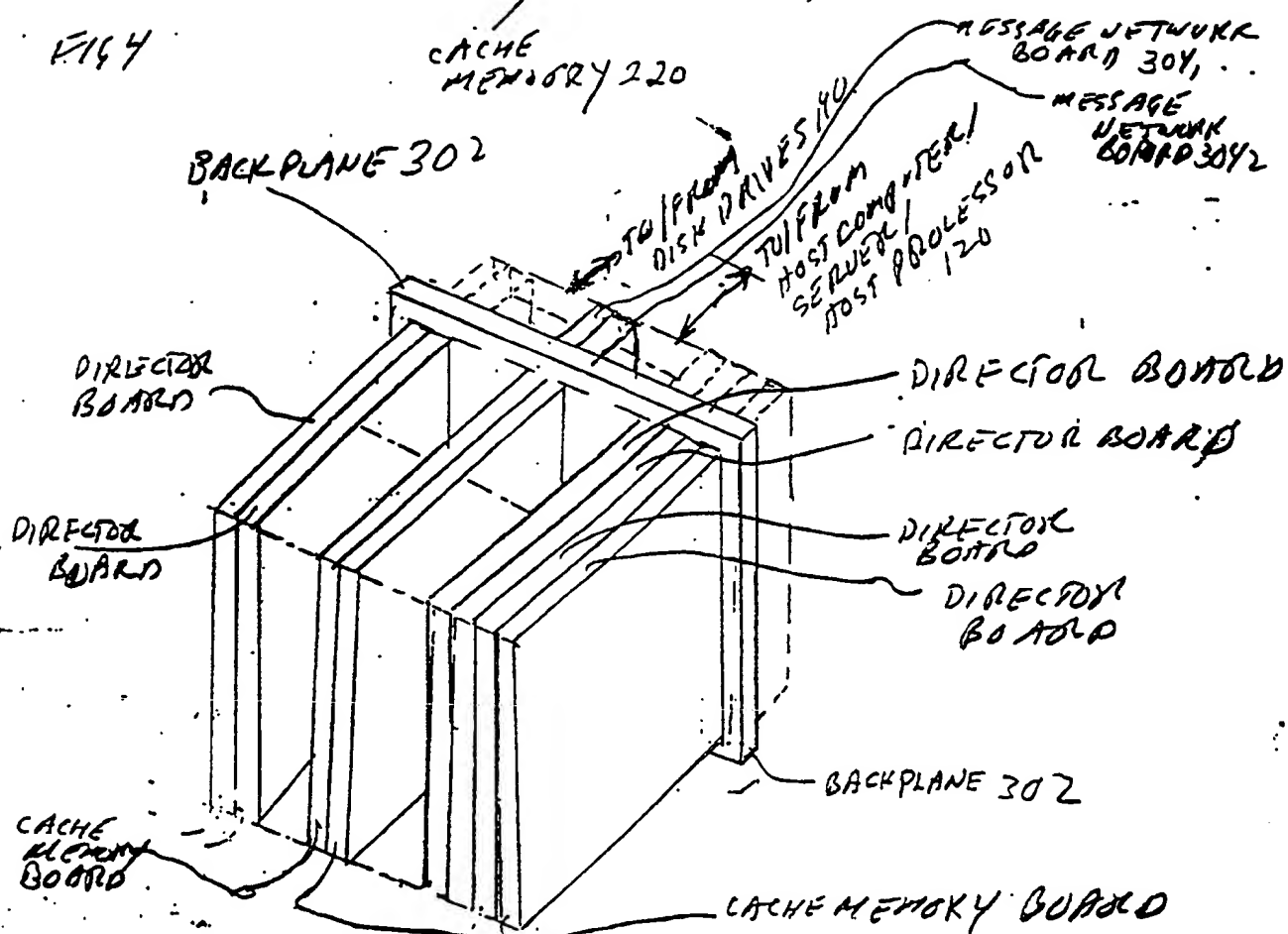


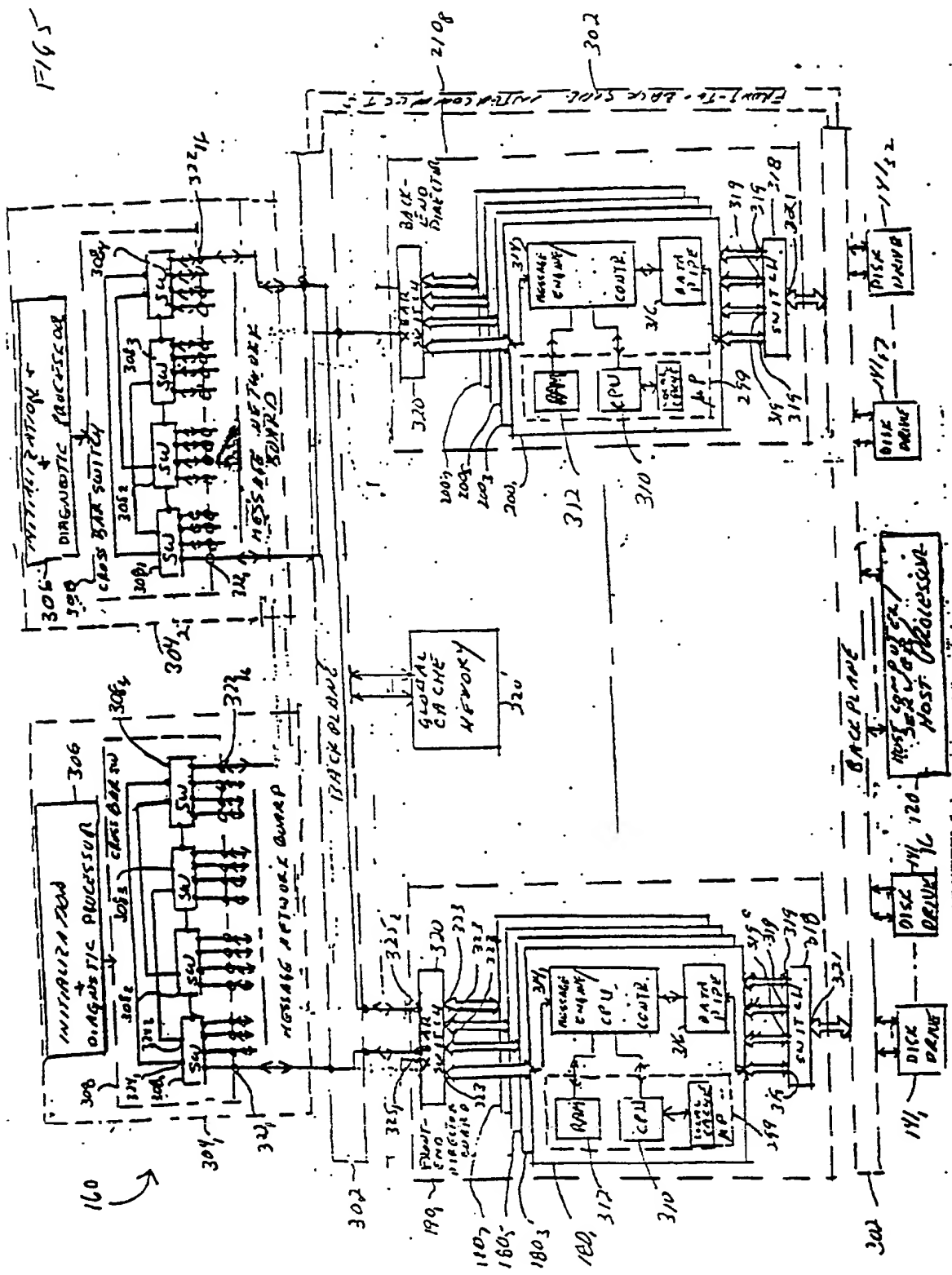
FIG. 4



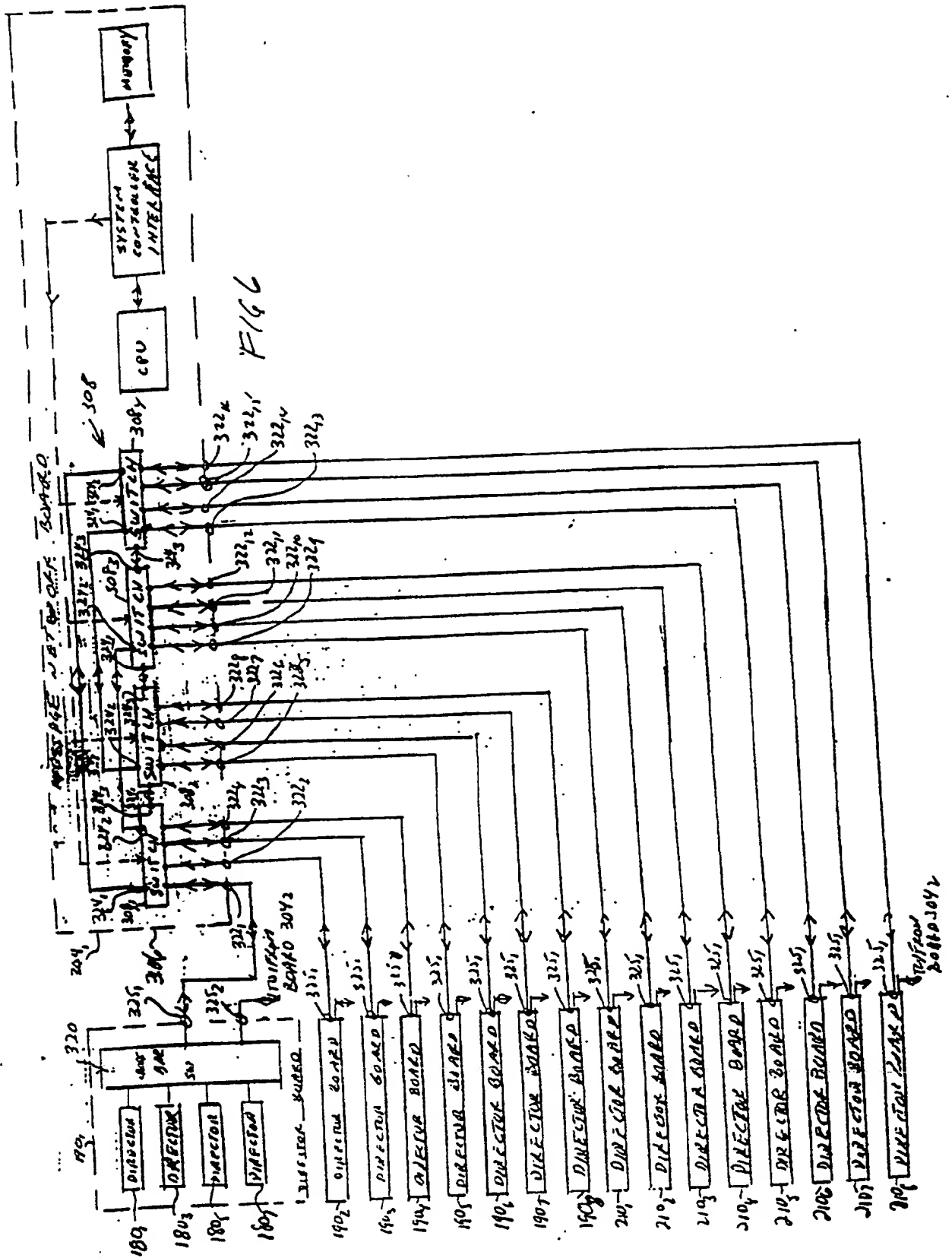
00000155-100500

005007 005000

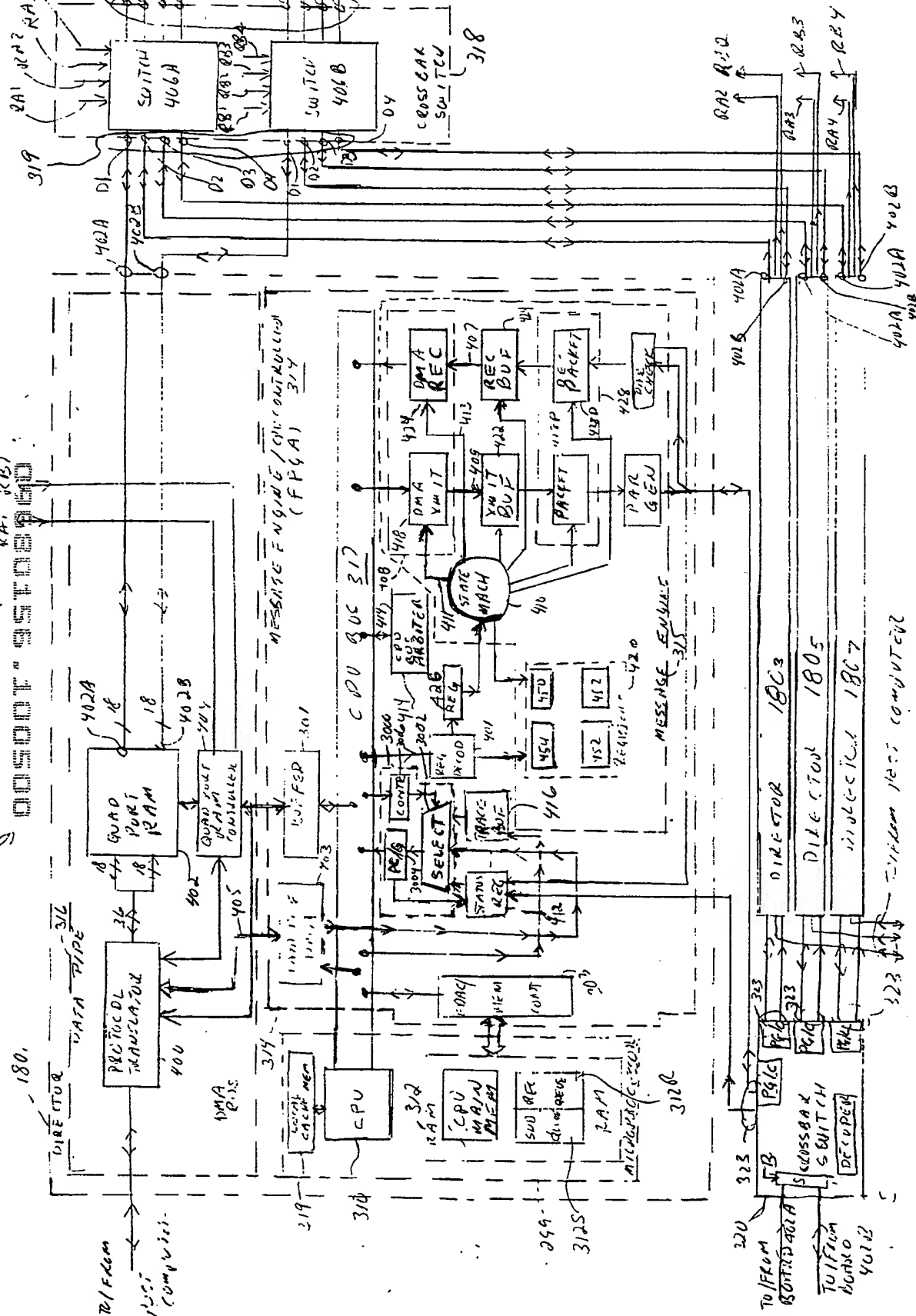
FIG 5



09680156 100500



2014 2015 1751



00500T 95050900

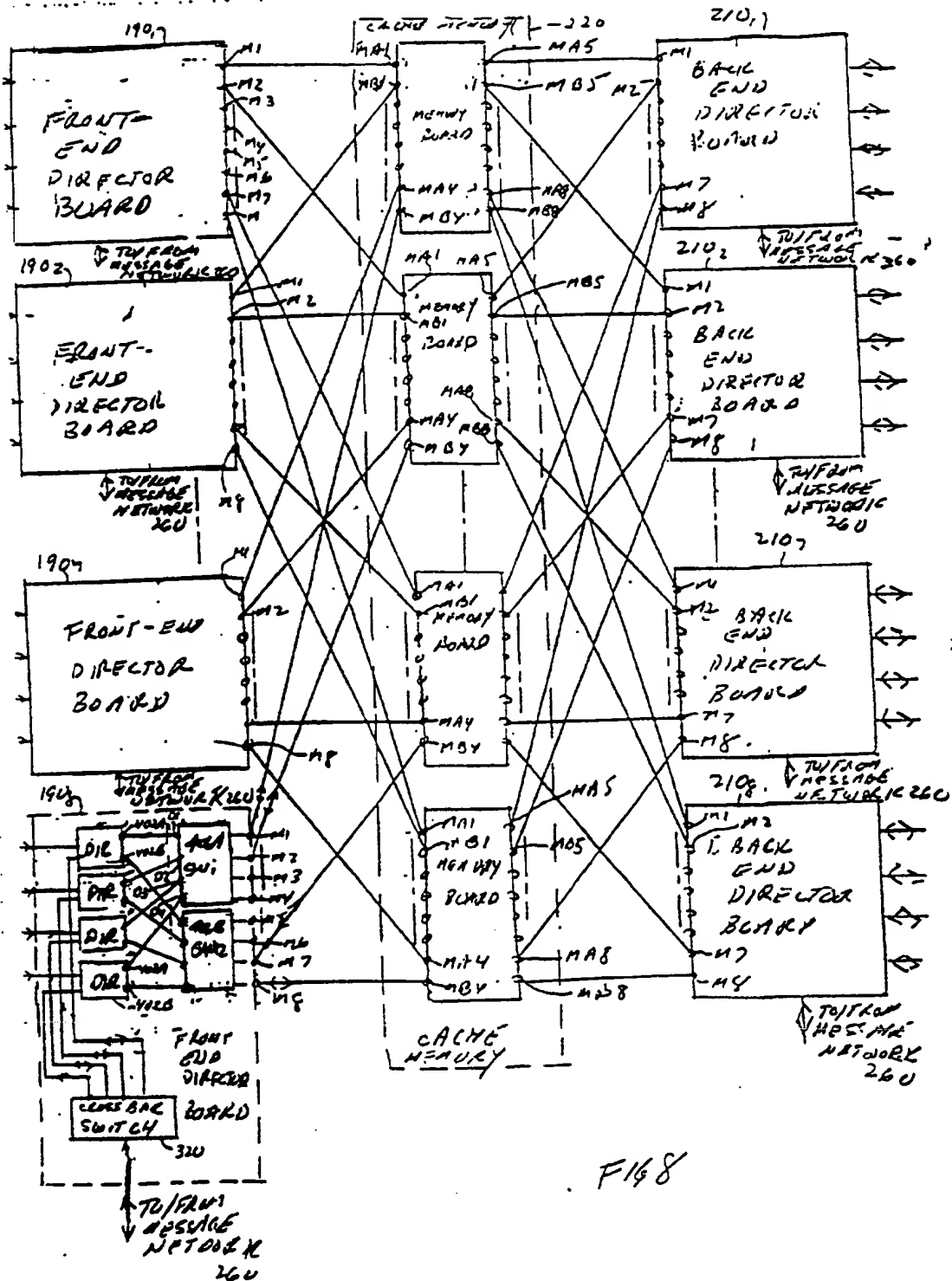
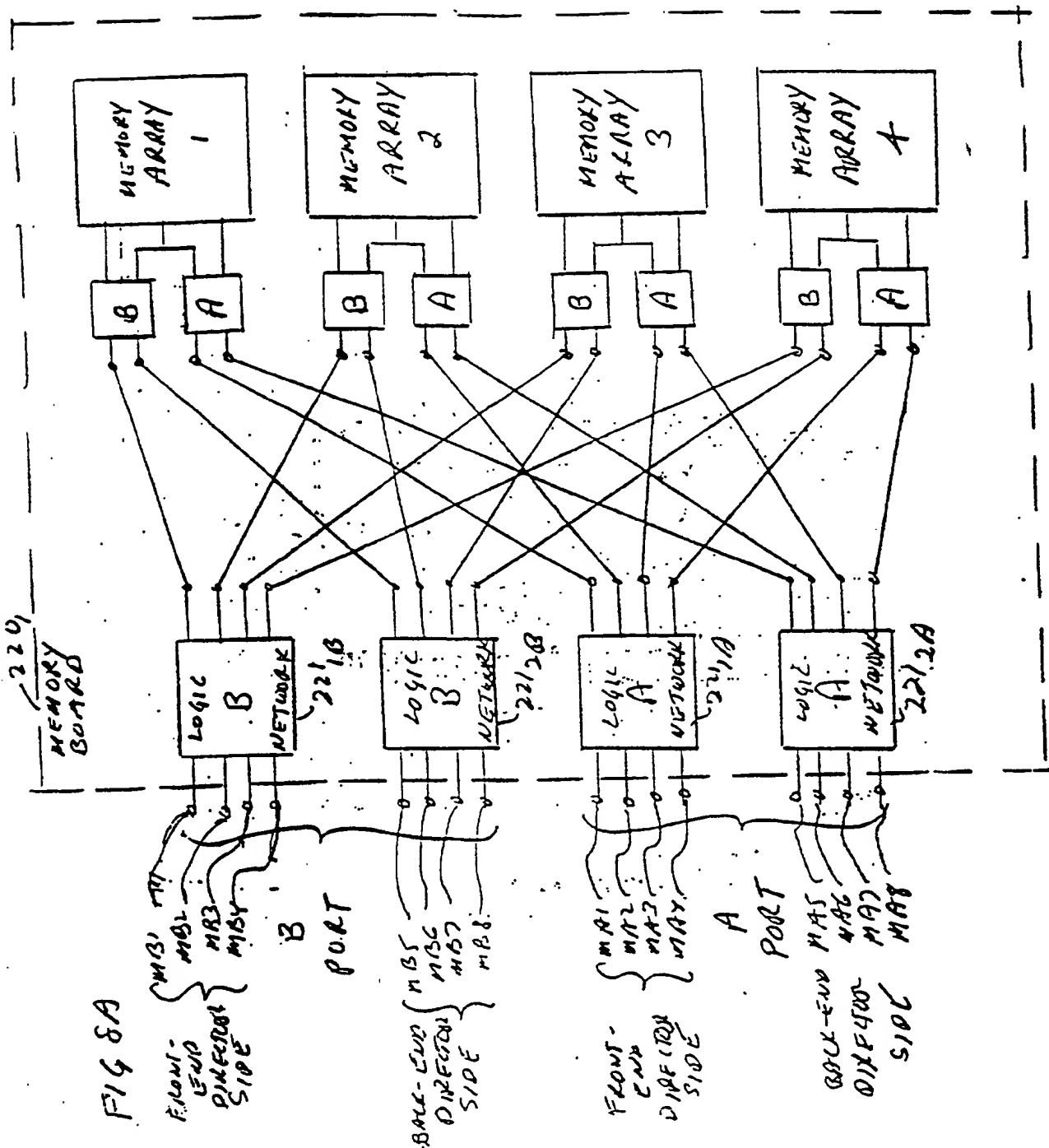


FIG 8

FIG 8A



SECRET

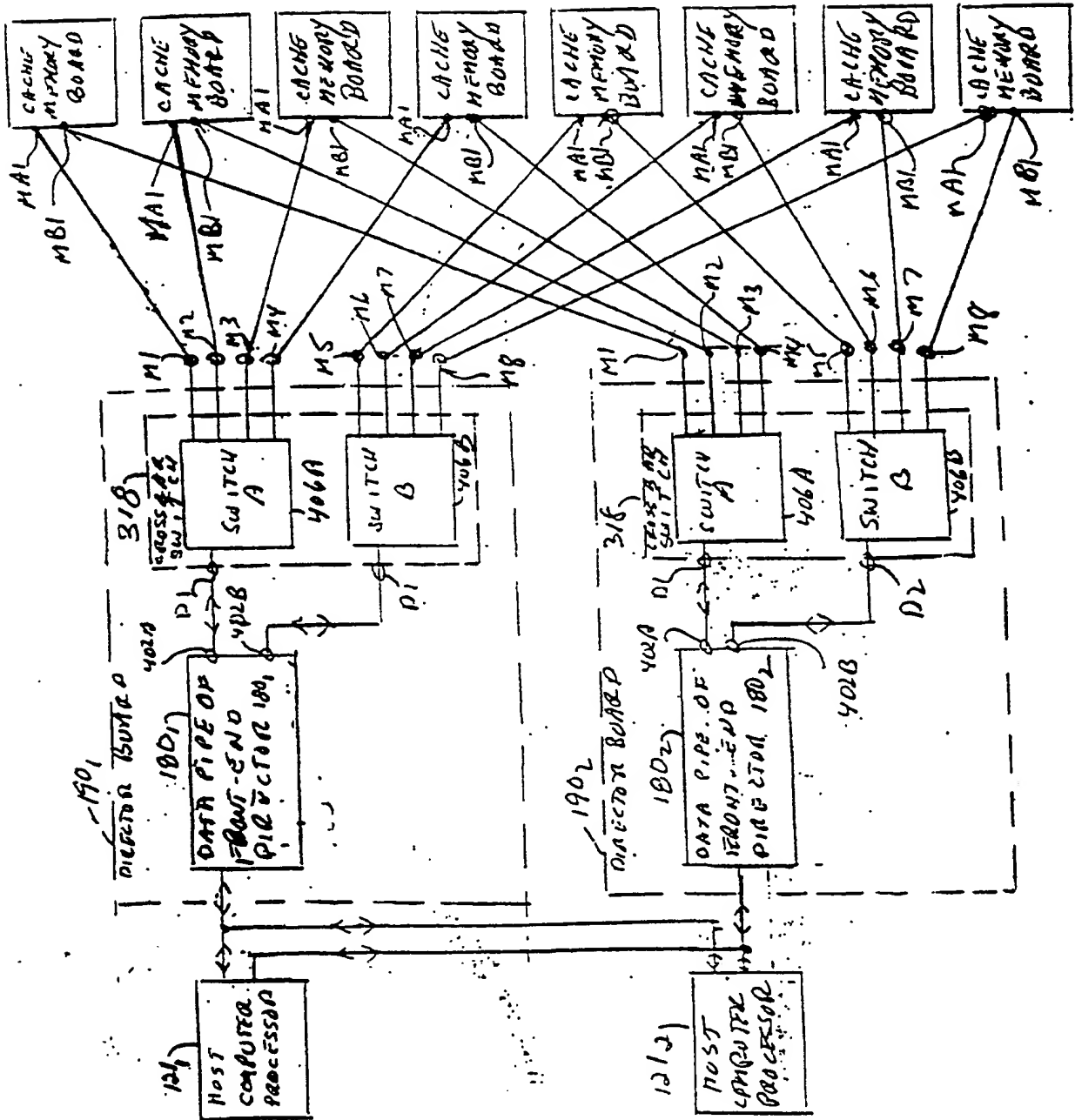
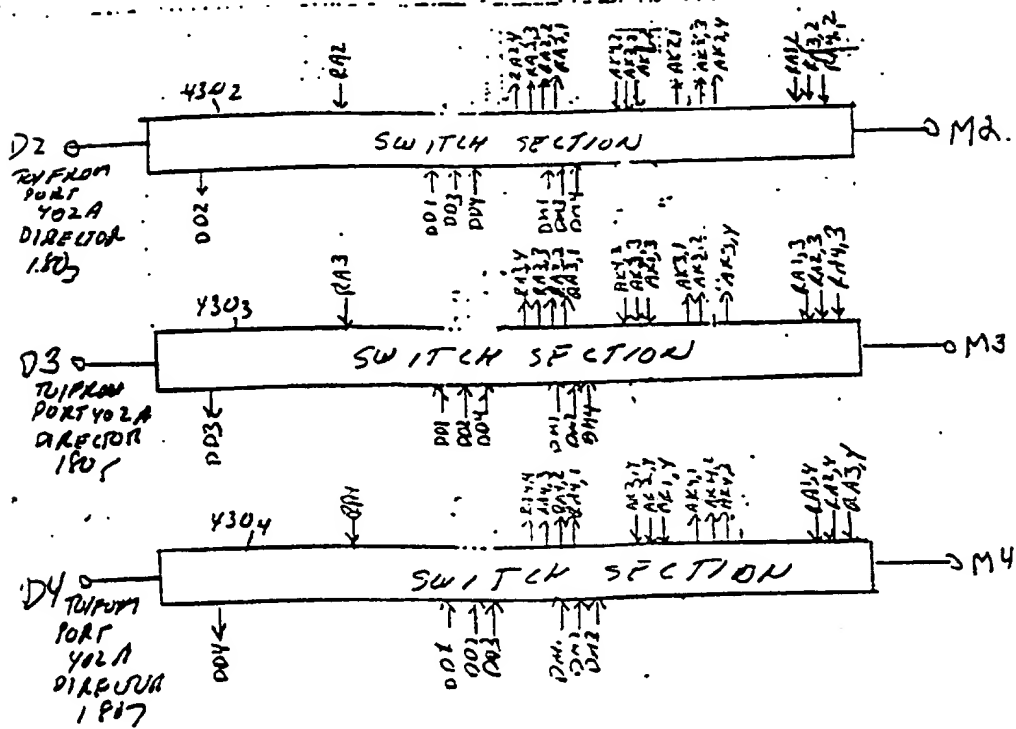
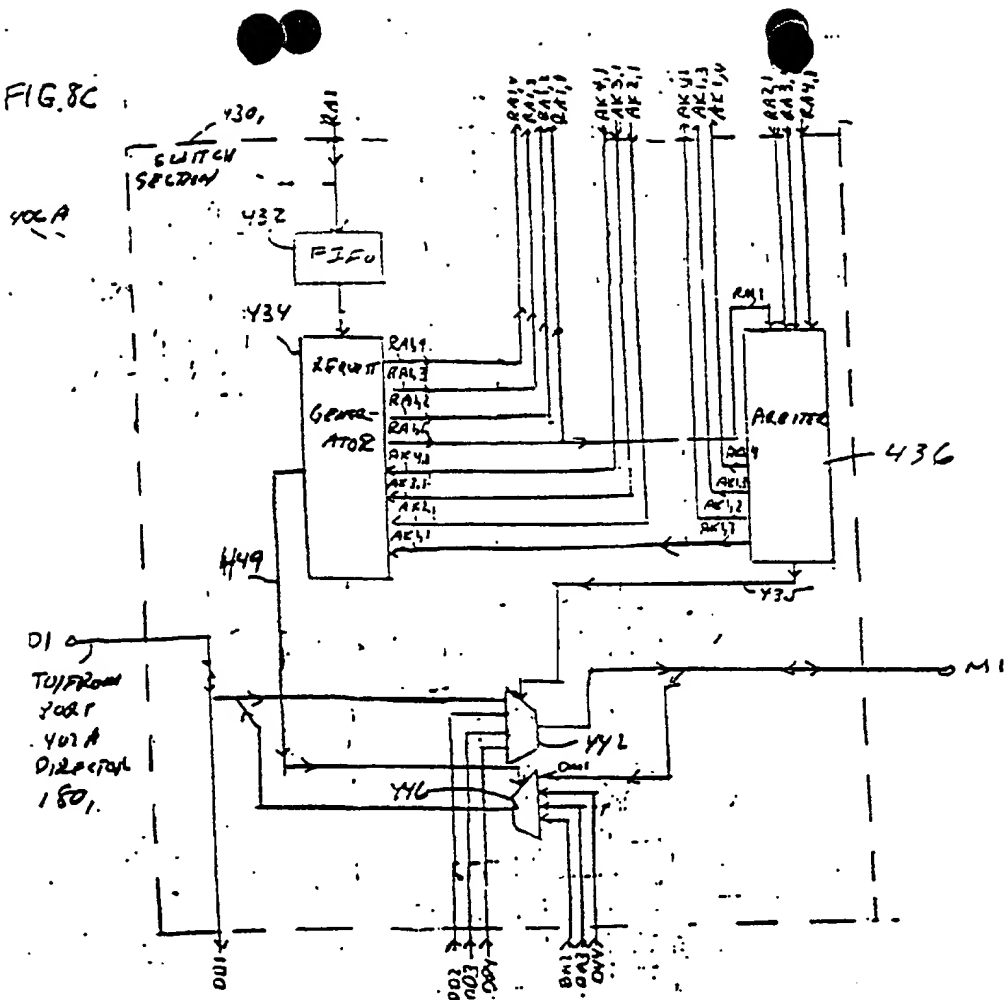


FIG. 8C



F169

CPU BUS 317

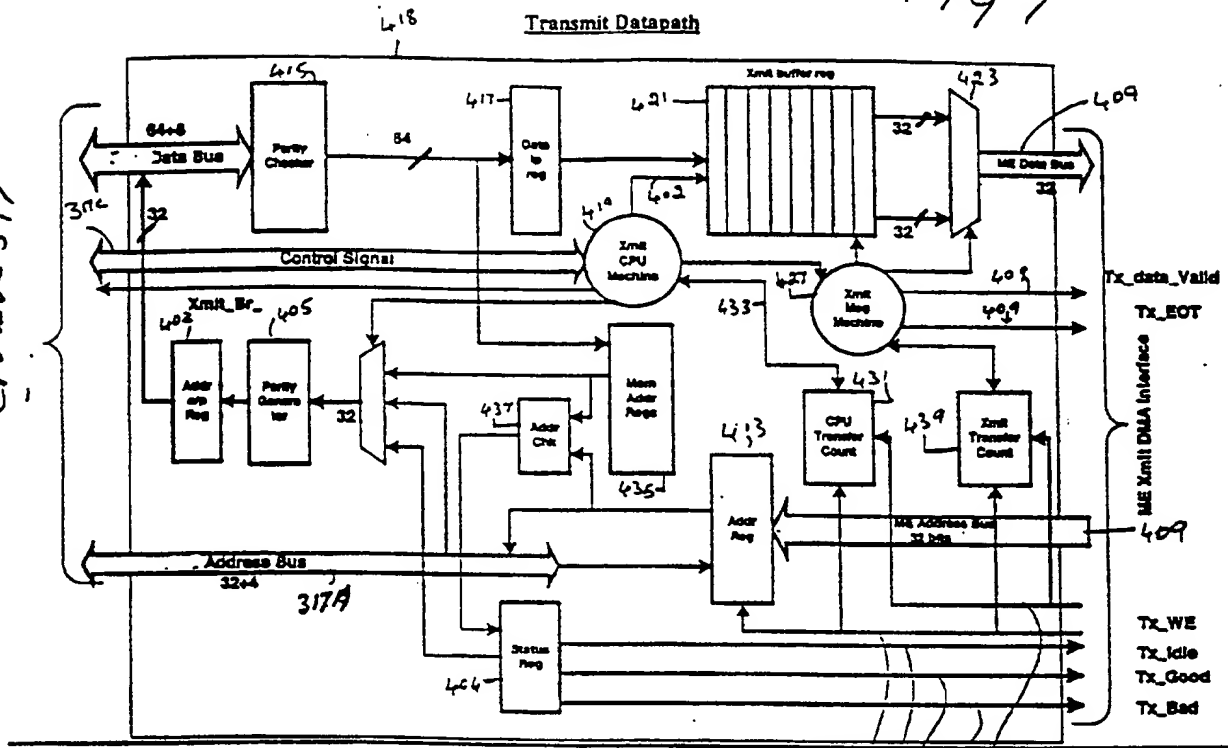
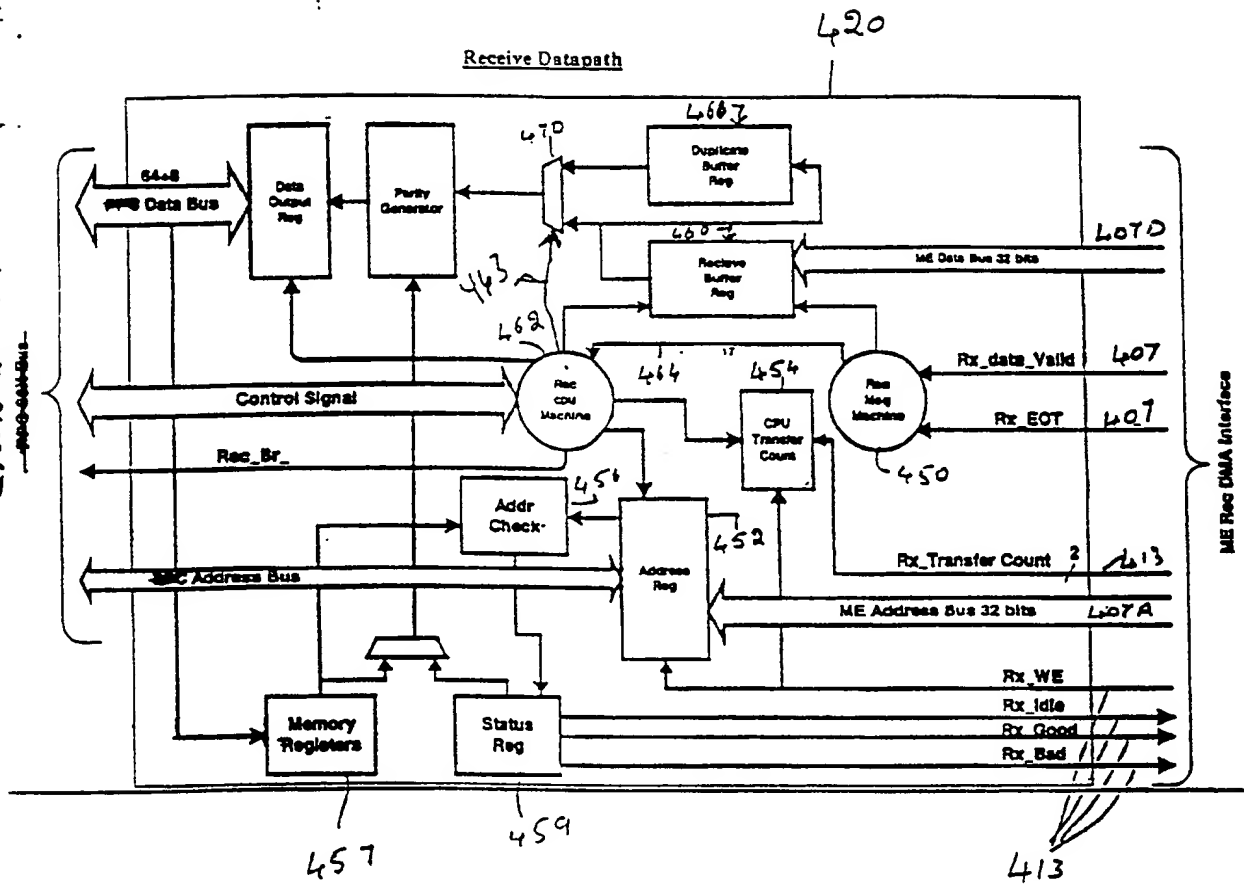


FIG 10



005001" 95102950
CPU BUS 317
-rec-out-bus-

Message Bus Send Operation

FIG. 11A

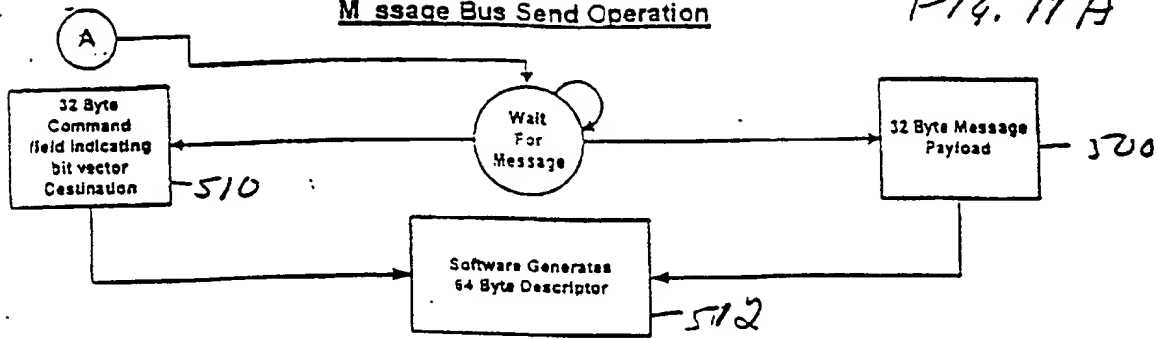


FIG. 11

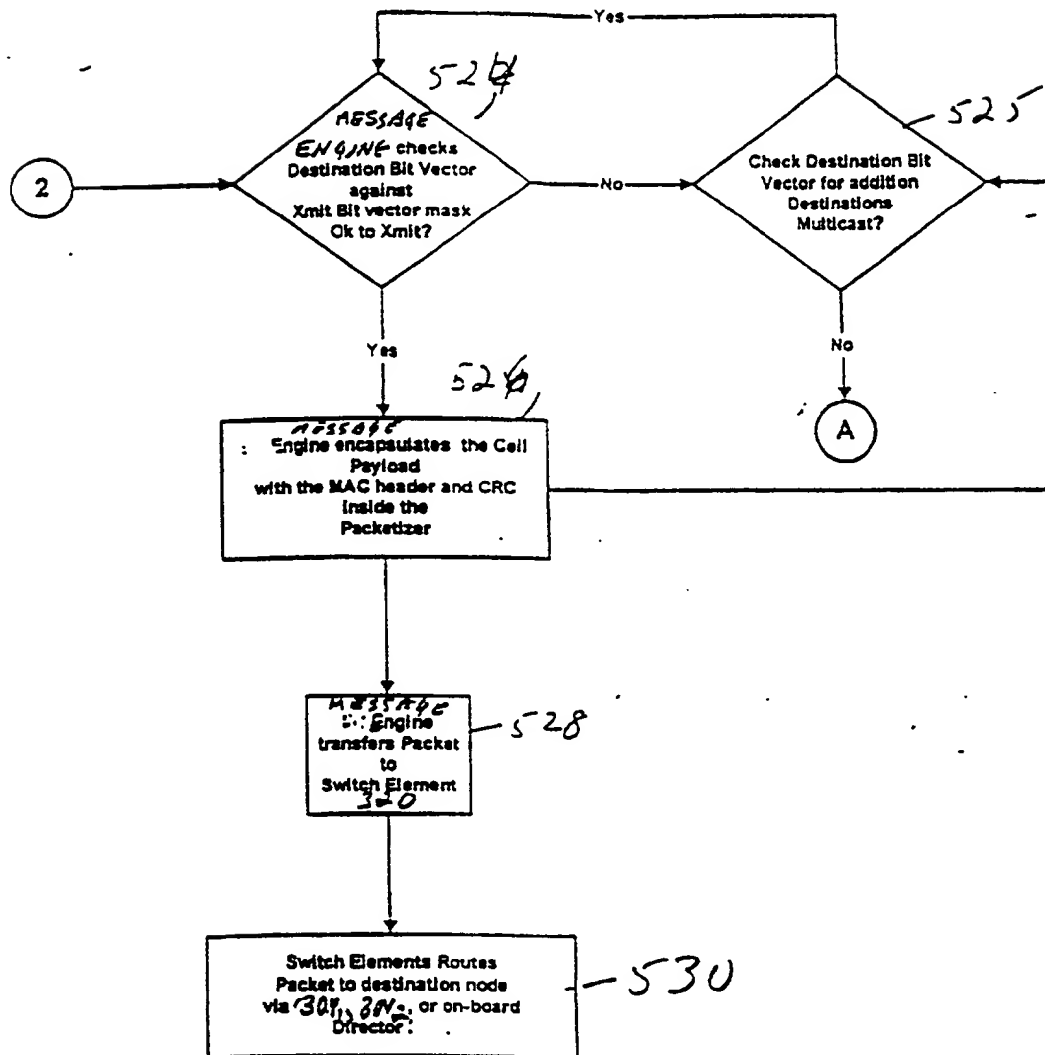
FIG. 11A

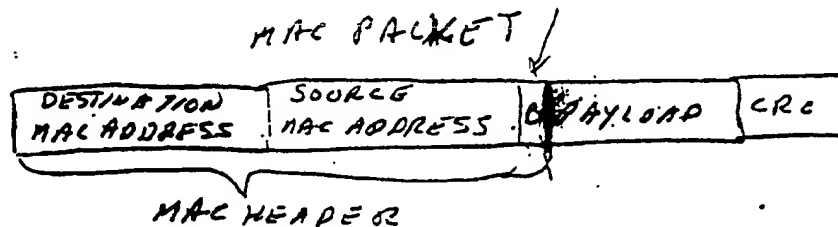
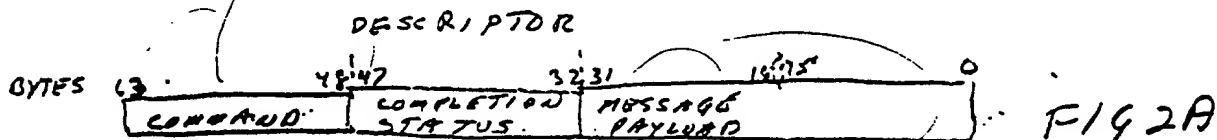
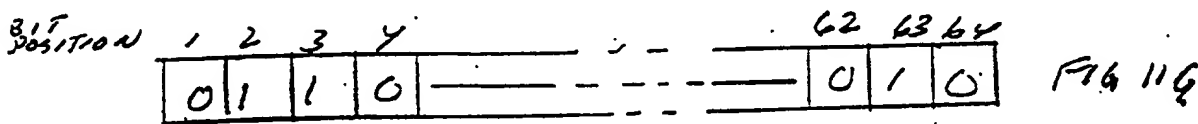
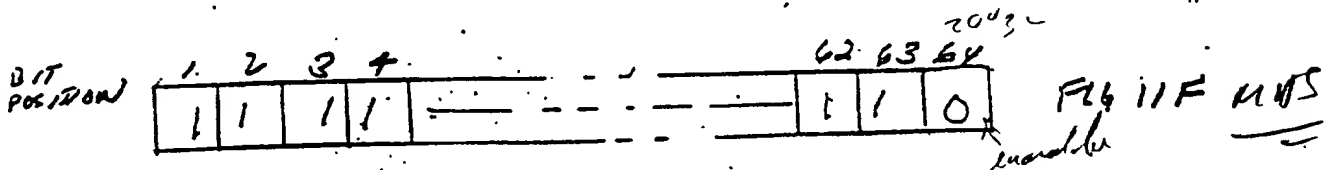
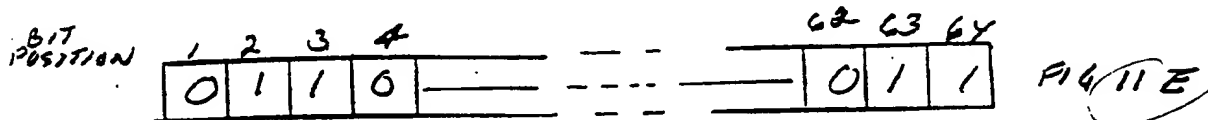
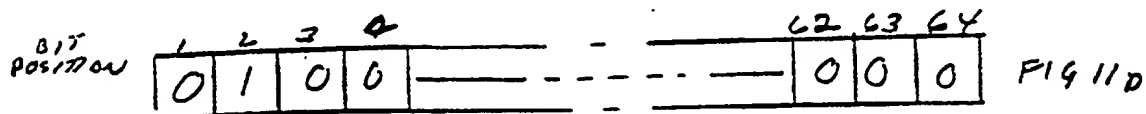
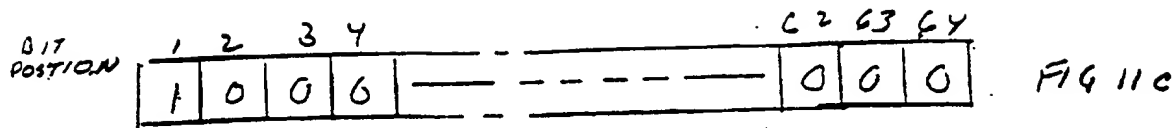
FIG. 11B

09680156-100500

Message Bus Send Operation Continued

F1611B





Message Bus Receive Operation

FIG. 12A

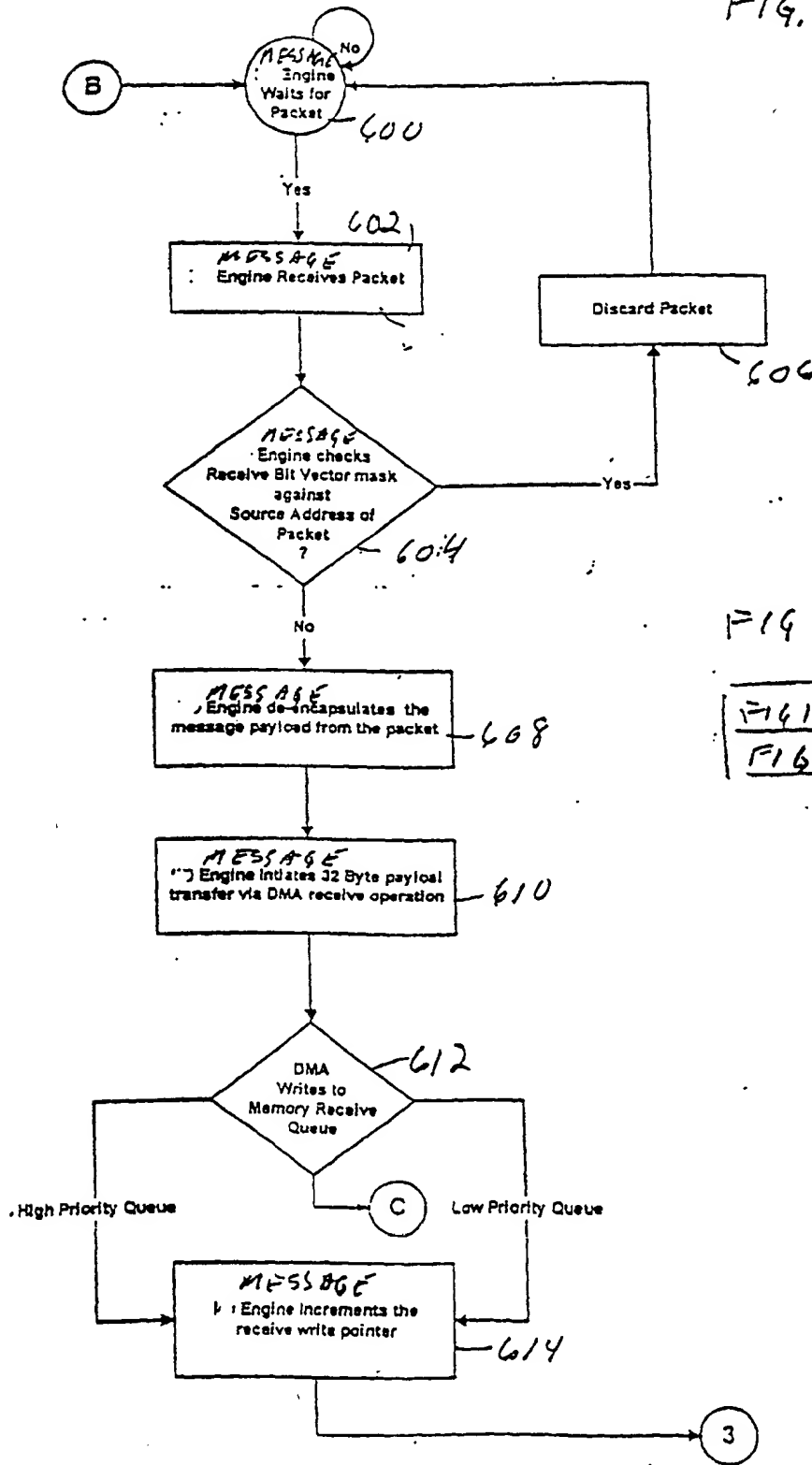
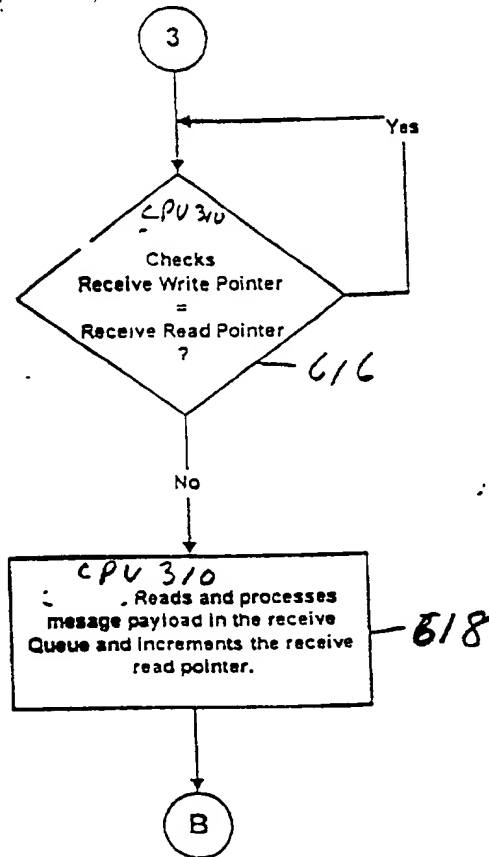


FIG 12

FIG 12A
FIG 12B

00000156 200500

Message Bus Receive Operation Continued



F1412B

09680156-100500

Message Bus Acknowledgement Operation

F-14.13

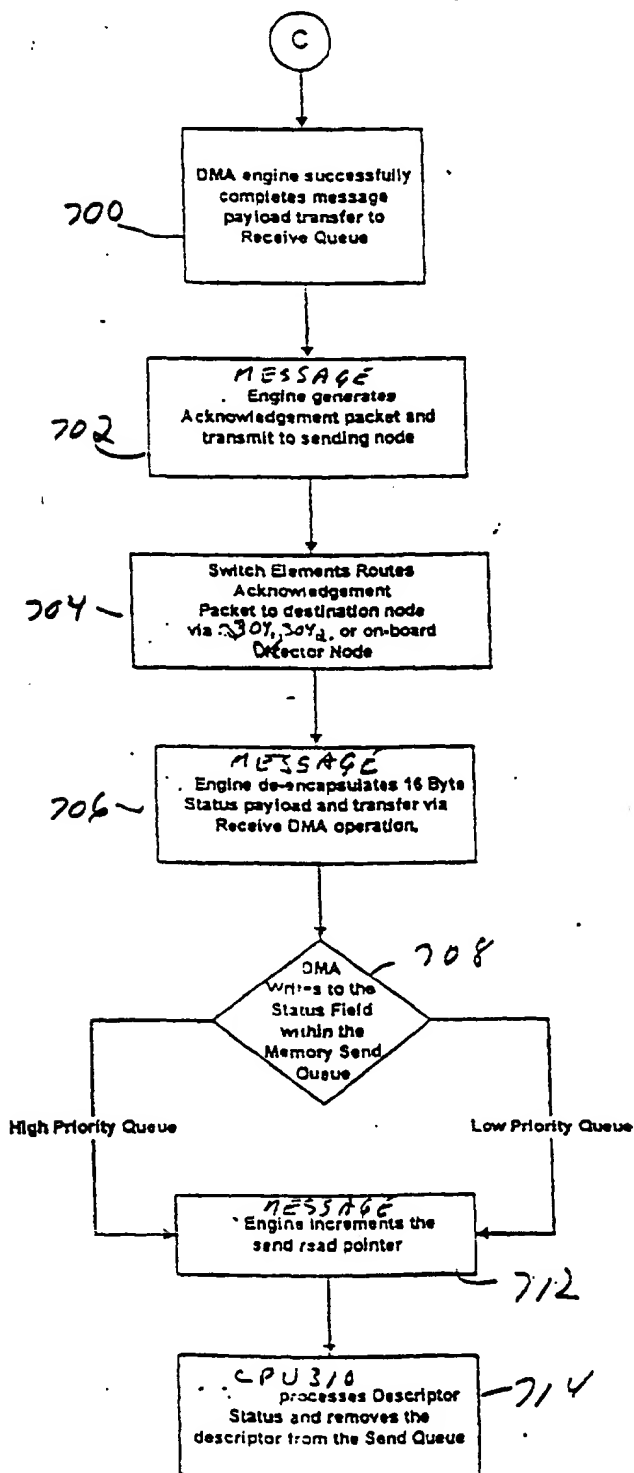
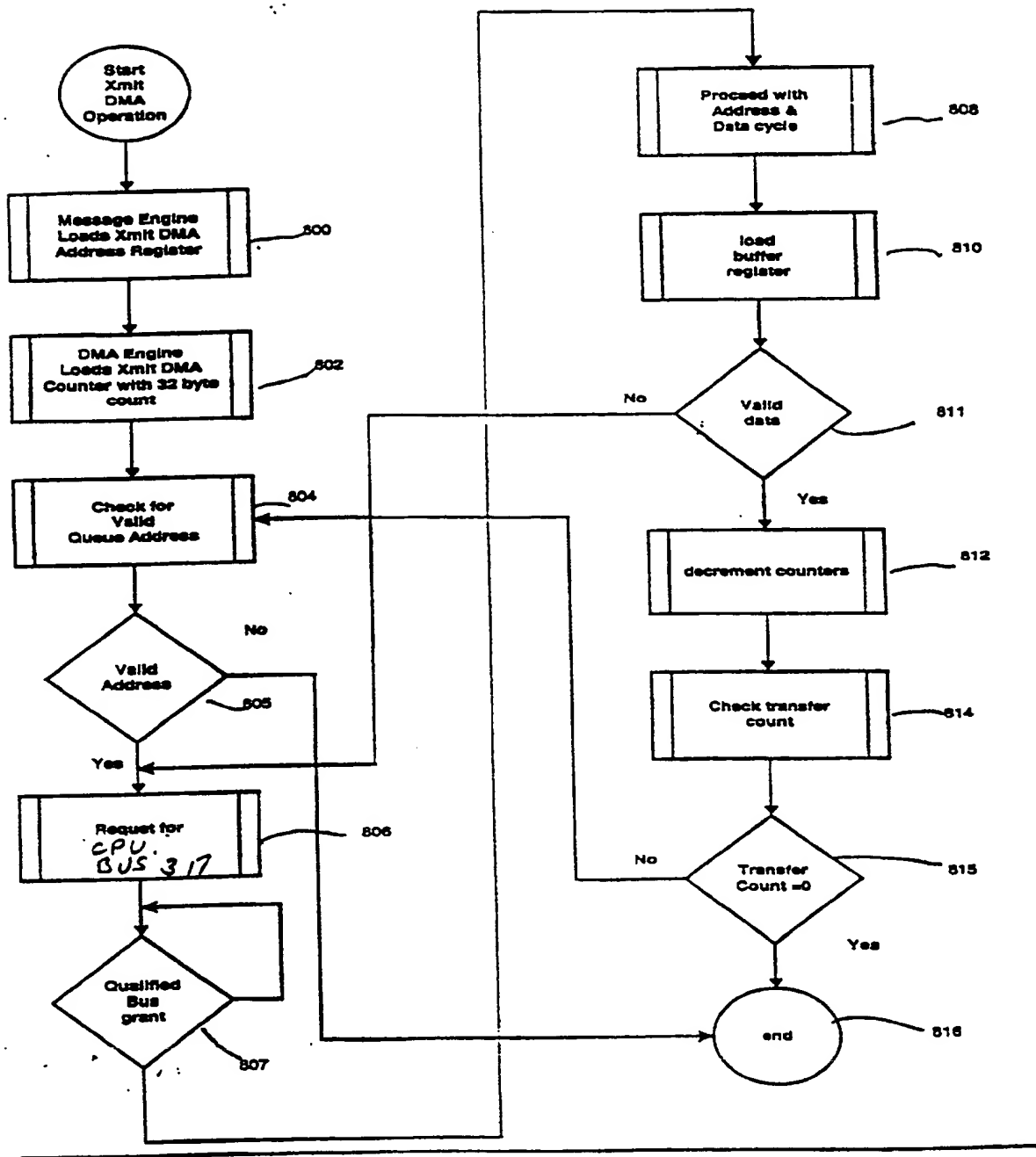


FIG 1A

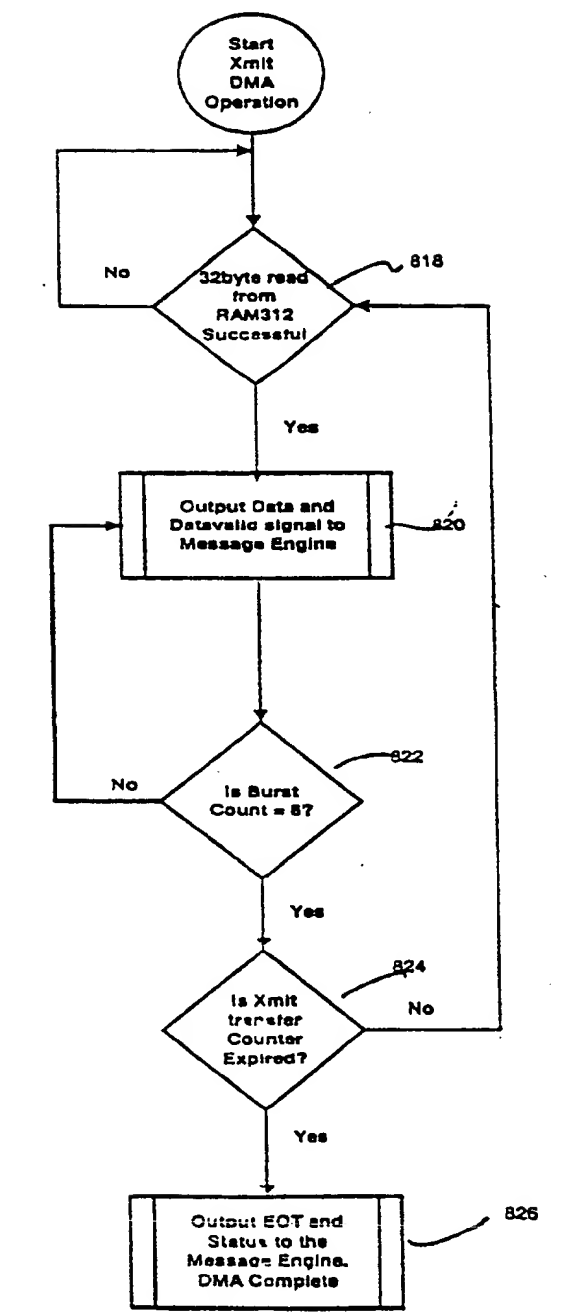
Xmit CPU flow



09680156 100500

F1414B

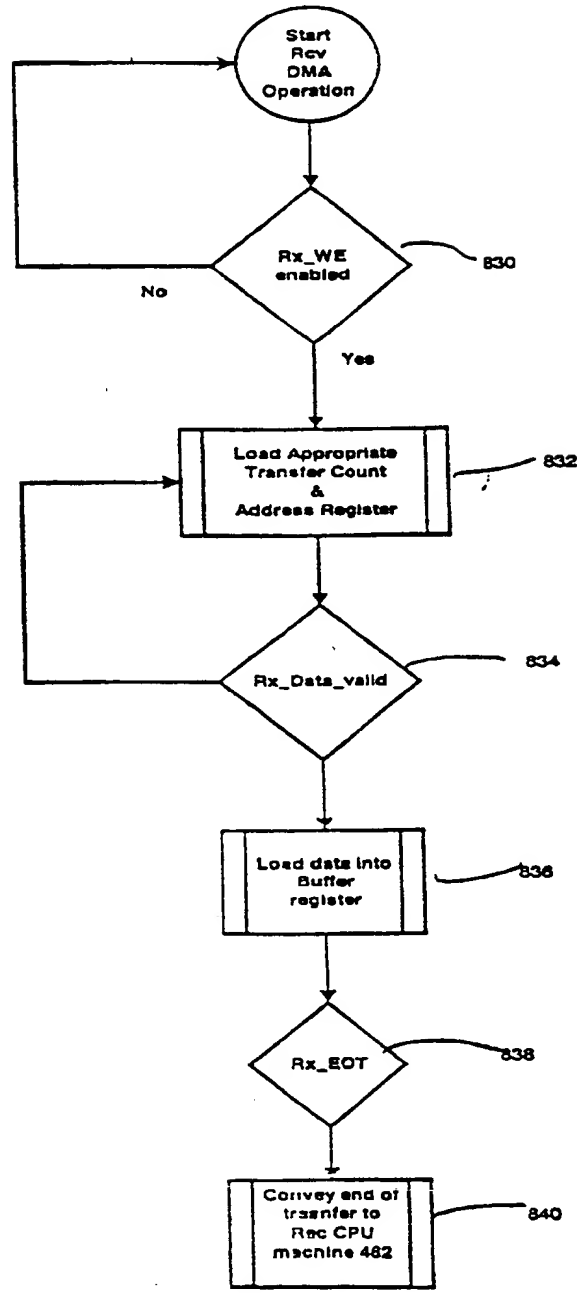
Xmit Msg flow



09580156-100500

FIG 15A

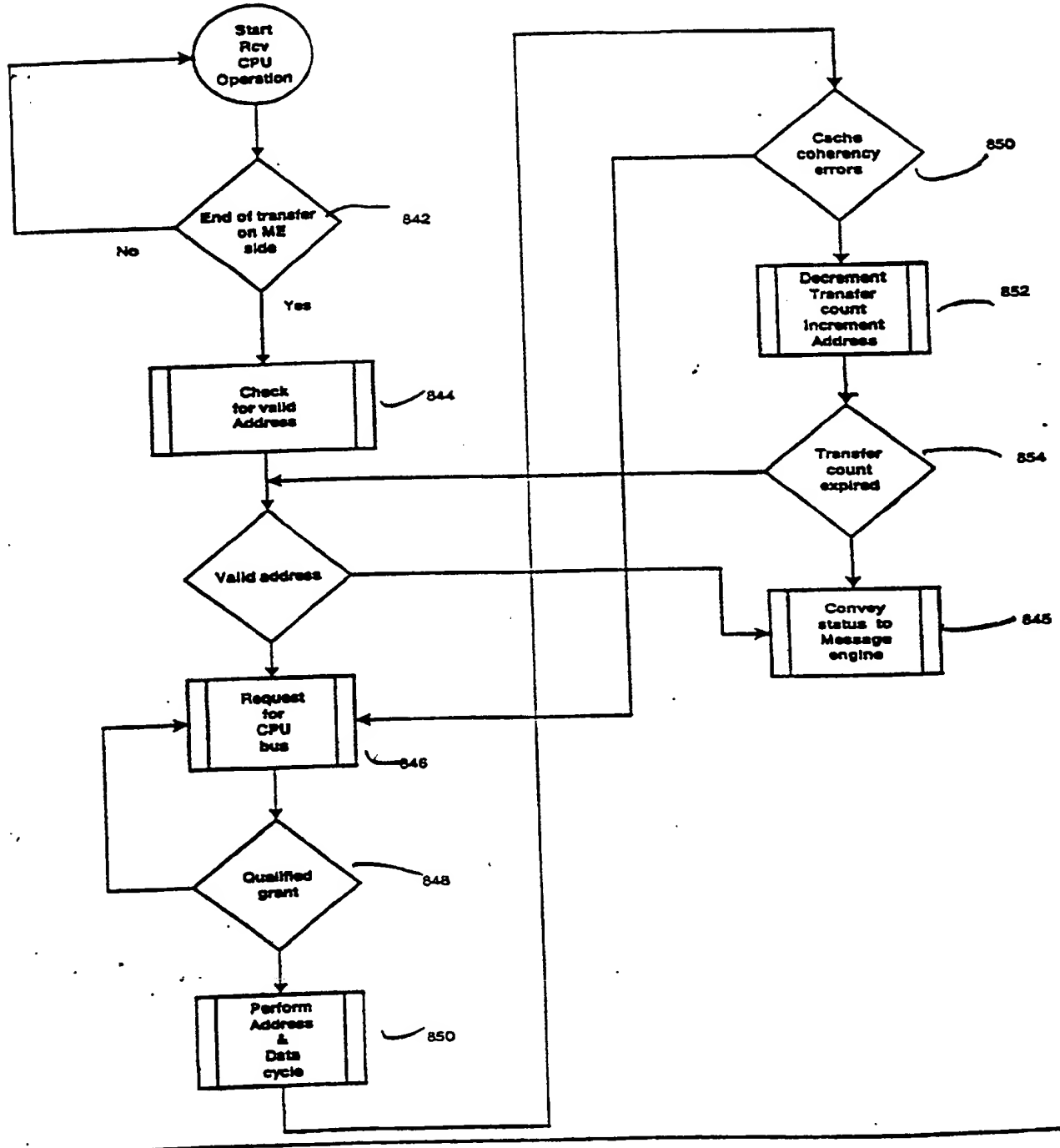
Rec msg flow



00500T 95T09960

F1615B

Rec cpu flow

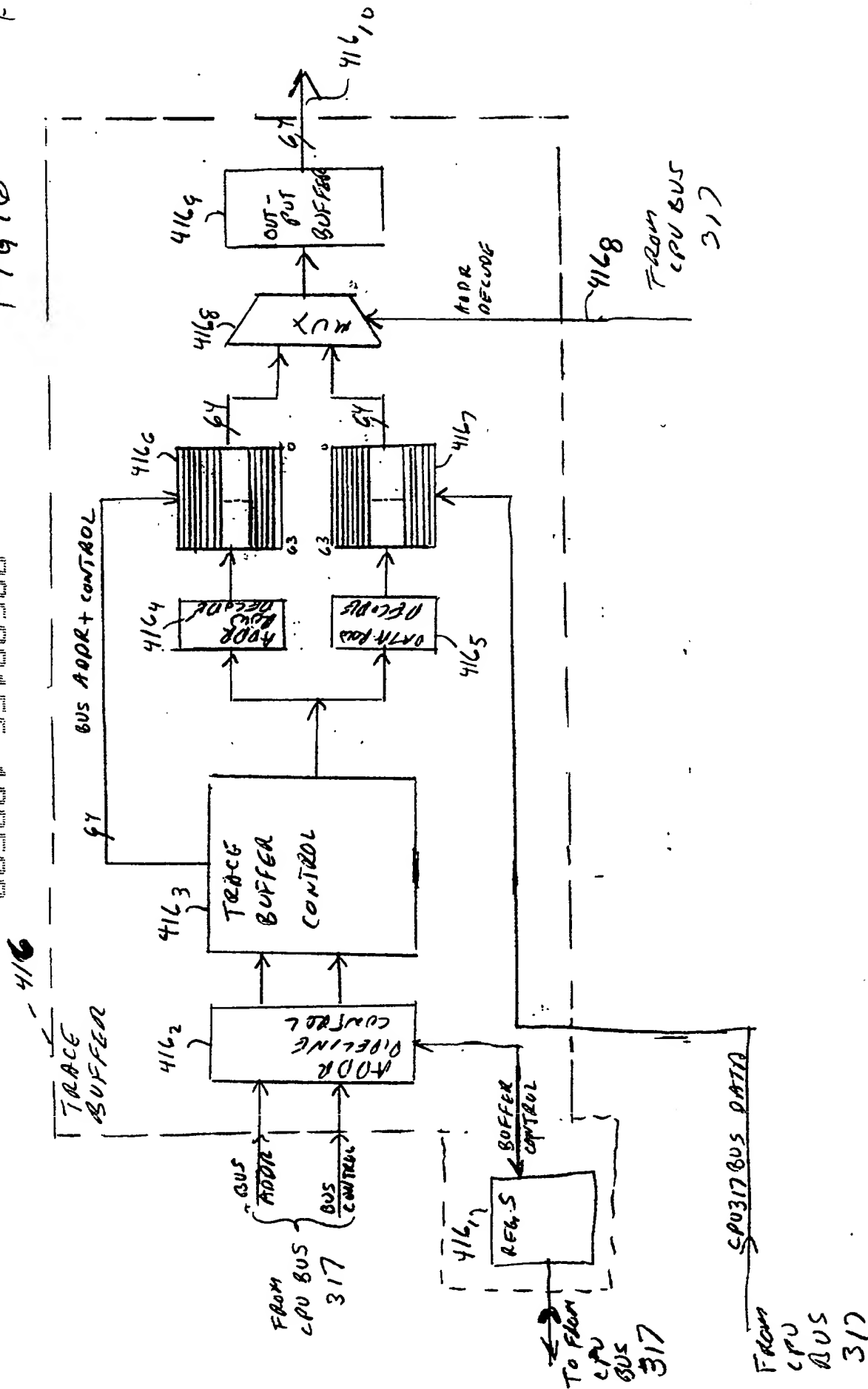


0050155 100500

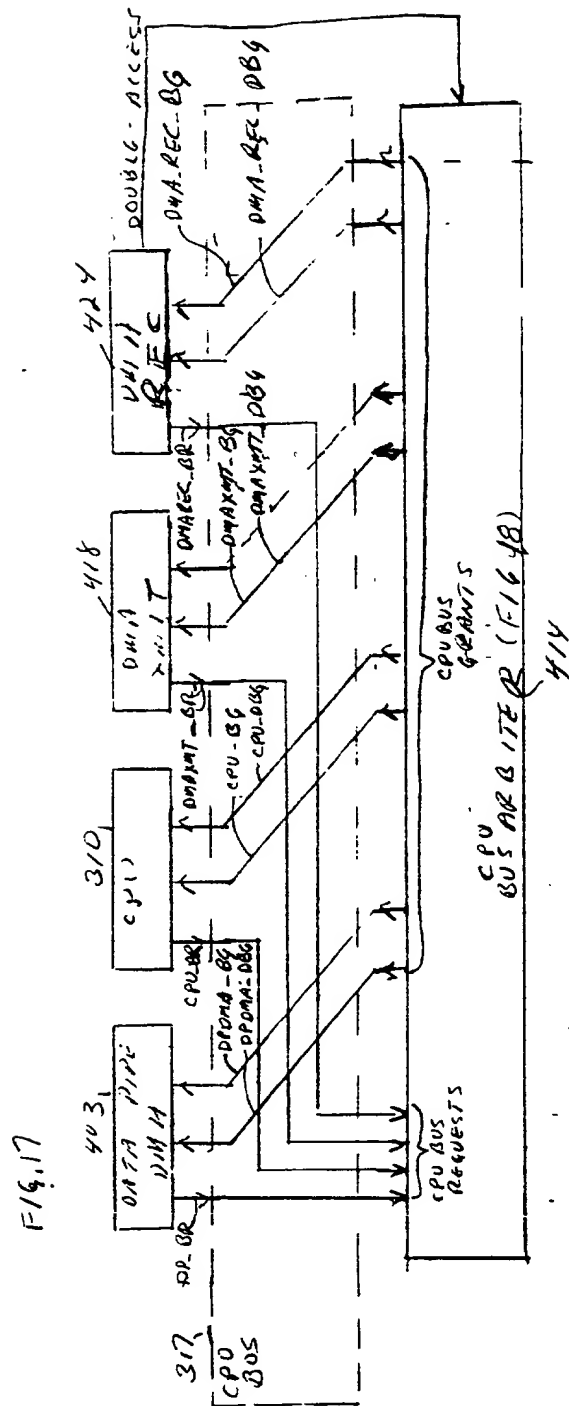
00500F "95F08960

Fig 16

F

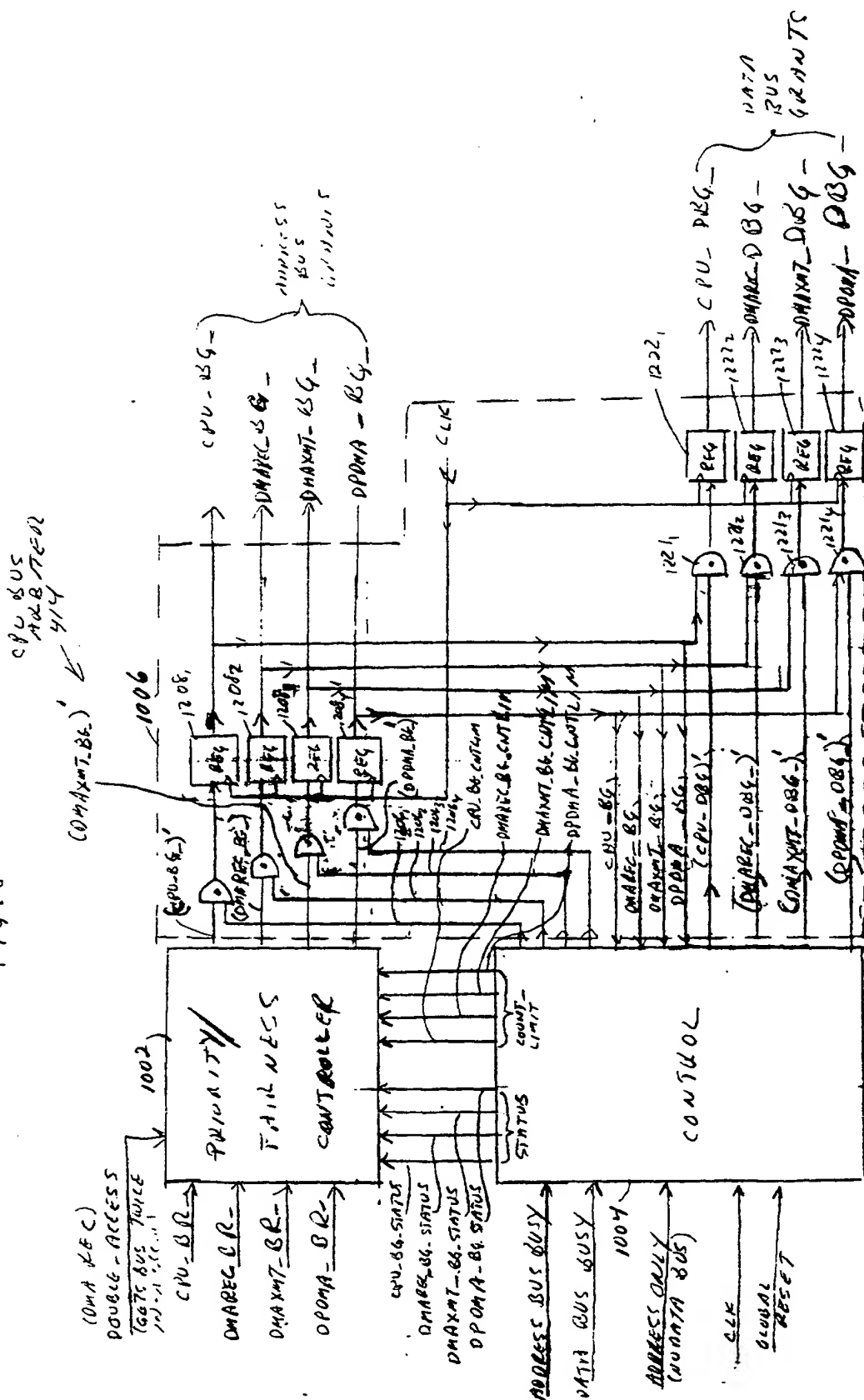


SECRET



00500T" 95T02950

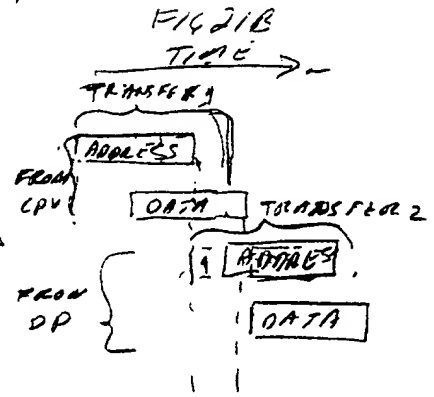
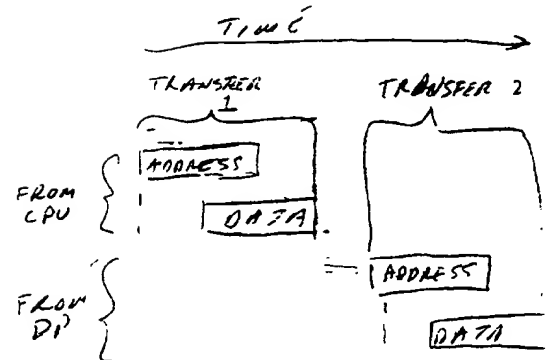
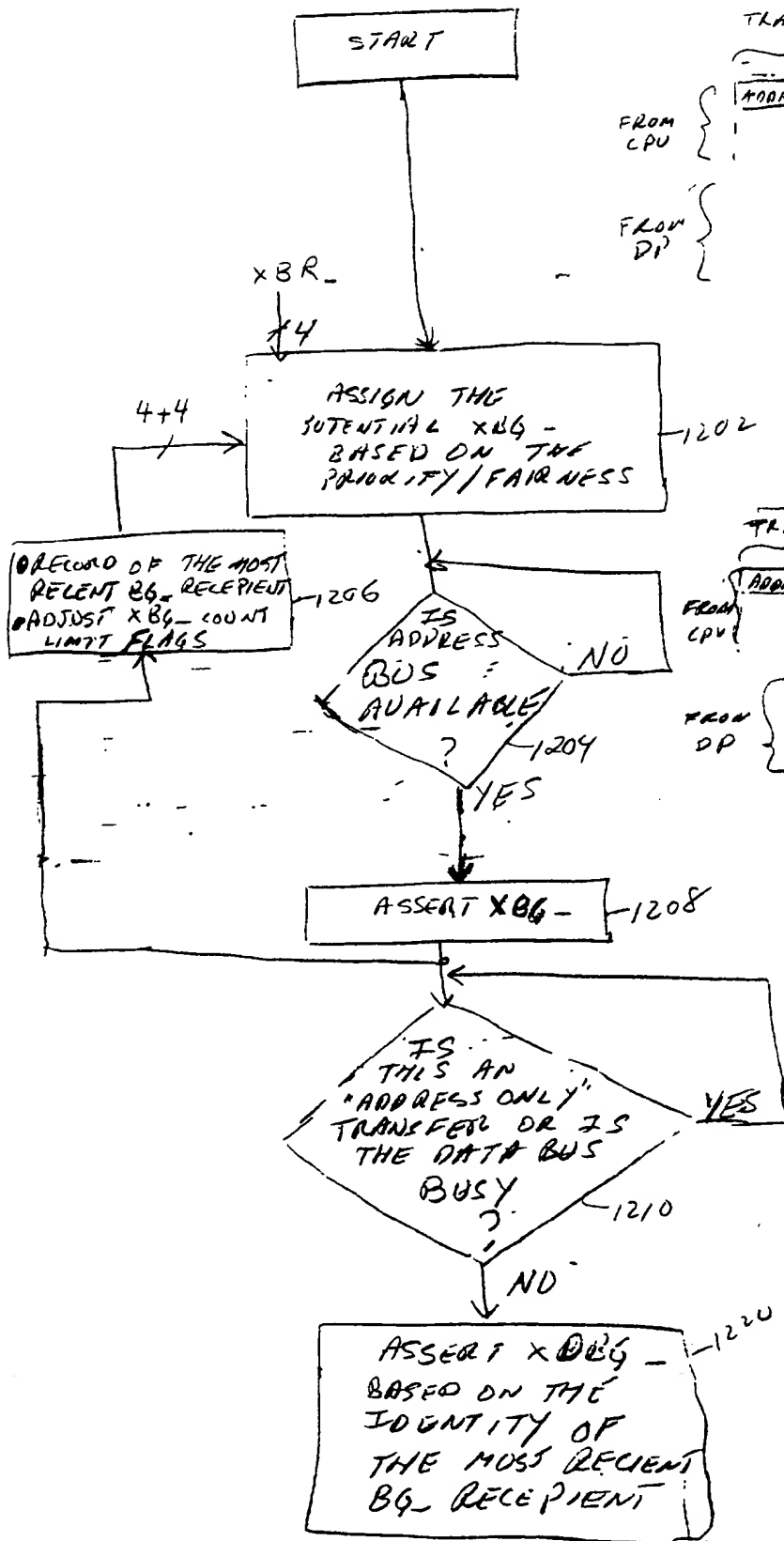
FIG 18



POTENTIAL GRANT TO:	CPU- OR	DMAREQ- BQ	DMAEXT- BQ	DPDMA- BQ	CPU-XG- BQ	DMAINT-BQ	DMAEXT-BQ	DMAINT-BQ	CPU-XG- CUTLIM	DMAEXT-BQ	DMAINT-BQ	DPDMA-BQ	DOUBLE- ACCESS
(CPU-EG)	PC	O	O	O	O	DC	DC	DC	PC	DC	DC	DC	PC
(CPU-EG)	I	DC	DC	DC	O	DC	DC	DC	DC	O	O	O	O
(CPU-EG)	I	DC	DC	DC	DC	DC	DC	DC	I	DC	DC	DC	O
(DMAREQ-BQ)	O	I	O	O	DC	DC	DC	DC	DC	DC	DC	DC	DC
(DMAREQ-BQ)	O	I	DC	DC	DC	DC	DC	DC	DC	DC	DC	DC	DC
(DMAREQ-BQ)	O	I	DC	DC	DC	DC	DC	DC	O	I	DC	DC	DC
(DMAEXT-BQ)	O	O	I	O	DC	DC	DC	DC	PC	DC	DC	DC	DC
(DMAEXT-BQ)	O	O	I	DC	DC	DC	DC	DC	DC	DC	DC	O	DC
(DMAEXT-BQ)	O	I	I	DC	DC	DC	DC	DC	PC	DC	DC	O	O
(DMAEXT-BQ)	I	O	I	DC	I	DC	DC	DC	DC	DC	DC	O	DC
(DMAEXT-BQ)	DC	DC	I	DC	DC	DC	DC	DC	O	O	I	DC	O
(DPDMA-BQ)	O	C	C	I	DC	DC	DC	DC	DC	DC	DC	DC	DC
(DPDMA-BQ)	O	O	I	I	DC	DC	DC	DC	DC	DC	DC	DC	DC
(DPDMA-BQ)	O	I	I	I	DC	DC	DC	DC	DC	DC	DC	DC	DC
(DPDMA-BQ)	I	O	I	I	I	DC	DC	DC	DC	DC	DC	DC	DC
(DPDMA-BQ)	I	DC	DC	I	DC	DC	DC	DC	O	O	O	I	O

FIG 20

FIG 2A



09680155 100500

